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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10023861	12/21/2001	438	6a4	1765	Tran, B

****APPLICANTS:** Kwon Jae;

****CONTINUING DATA VERIFIED:**
None BT

**** FOREIGN APPLICATIONS VERIFIED:**
REPUBLIC OF KOREA 2001-60631 09/28/2001
BT

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials BT	ATTORNEY DOCKET NO 10729-P6746US0
TITLE : Method of semiconductor device isolation	

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
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